

Docket No.: 94100417(EP)USD1X1C1D6 PDDD
Serial No.: 09/773,473

PATENT
Art Unit 2154

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (CURRENTLY AMENDED) A multi-standard decoder for decoding [a] data streams comprising:

processing stages interconnected to form a pipeline and for processing tokens derived from one or more of the data streams, the processing stages including standard-independent and standard-dependent processing stages, the standard-dependent processing stages capable of reconfiguration to operate in accordance with different data encoding standards; and

wherein the tokens provide reconfiguration information to the standard-dependent processing stages.

2. (ORIGINAL) The multi-standard decoder of claim 1, wherein each of the tokens includes an extension indicator that indicates whether additional words are present.

3. (ORIGINAL) The multi-standard decoder of claim 1, wherein one of the standard-dependent processing stages comprises an inverse quantizer.

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4. (ORIGINAL) The multi-standard decoder of claim 3, wherein one of the tokens comprises a first QUANT_TABLE token.
5. (ORIGINAL) The multi-standard decoder of claim 4, wherein the inverse quantizer recognizes the first QUANT_TABLE token and, responsive to a first state of the extension indicator in a first word of the first QUANT_TABLE token, generates a second QUANT_TABLE token to be conveyed to another of the processing stages.
6. (ORIGINAL) The multi-standard decoder of claim 5, wherein the second QUANT_TABLE token includes quantization table values.
7. (ORIGINAL) The multi-standard decoder of claim 4, wherein responsive to a second state of the extension indicator of the first word of the QUANT_TABLE token, the inverse quantizer installs a quantization table of the first QUANT_TABLE token in a memory.
8. (CURRENTLY AMENDED) A method of decoding [a] data streams of data encoded by different standards comprising:

receiving tokens at a standard-dependent processor, the standard-dependent processor capable of reconfiguration to operate in accordance with the different standards; and

reconfiguring for standard-dependent processing in response to the received tokens.

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9. (ORIGINAL) The method of claim 8, wherein each token includes an extension indicator that indicates whether additional words are present and has a first and a second state to indicate reconfiguration information.

10. (ORIGINAL) The method of claim 8, wherein one of the conveyed tokens is a first QUANT_TABLE token, and further comprising:
recognizing the first QUANT_TABLE token; and

responsive to the first state of the extension indicator in a first word of the first QUANT_TABLE token, generating a second QUANT_TABLE token to be conveyed to another processor.

11. (ORIGINAL) The method of claim 7, wherein the second QUANT_TABLE token includes quantization table values to be used by the another processor.

12. (ORIGINAL) The method of claim 9, further comprising:
responsive to a second state of the extension indicator of the first word of the QUANT_TABLE token, installing a quantization table of the first QUANT_TABLE token in memory.

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13. (ORIGINAL) A system comprising:

processing stages including standard-independent and standard-dependent processing stages, the standard-dependent processing stages capable of reconfiguration to operate in accordance with different data encoding standards; and

tokens for interacting with the processing stages, the tokens providing reconfiguration information to the standard-dependent processing stages to cause the standard-dependent processing stages to reconfigure.